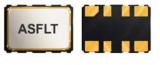
TIGHT STABILITY INDUSTRIAL GRADE CRYSTAL OSCILLATOR

ASFLT SERIES





5.0 x 3.2 x 1.05mm

> FEATURES:

- Highly reliable seam-sealed package
- Low current consumption
- Low phase noise and jitter
- Industrial grade tight temperature stability (±5.0ppm / -40 to +85°C)
- Fast start-up time
- CMOS output with Tri-state function

> APPLICATIONS:

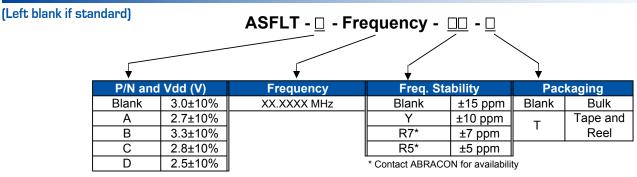
- Home networking by AC socket
- Wireless LAN
- Mobile communications
- PLC modem
- WiMax

STANDARD SPECIFICATIONS:

PARAMETERS	
ABRACON P/N:	ASFLT Series
Frequency:	4.000MHz to 54.000MHz
Standard Frequencies:	5, 10, 12, 16, 16.384, 19.44, 20, 24, 26, 32, 40, 44MHz
Output level:	CMOS
Operating temperature:	- 40°C to +85°C
Storage temperature:	- 40°C to +85°C
Overall Frequency Stability*:	±15ppm (see option)
Supply voltage (Vdd):	3.0Vdc ± 10% (see option)
Supply current (Idd):	7mA max.
Stand-by current:	10μA max.
Symmetry:	45/55 % @ 50% Vdd
Rise and Fall Times(Tr/Tf):	5ns max. / 10%Vdd-90%Vdd
Start-up time:	0.2ms typical, 3ms max.
Output load:	15pF max.
Output Voltage:	VOH = 0.9*Vdd min.
	VOL = 0.1*Vdd max.
Aging @ 25°C:	±2 ppm/ first year, ±7 ppm/ 10 years
Phase Noise (@ 10kHz offset):	-143dBc/Hz Typ.
Jitter:	1σ 3ps typ.

^{*} Overall frequency stability includes initial tolerance @ +25° C, and temperature stability.

> OPTIONS AND PART IDENTIFICATION:





TIGHT STABILITY INDUSTRIAL GRADE CRYSTAL OSCILLATOR

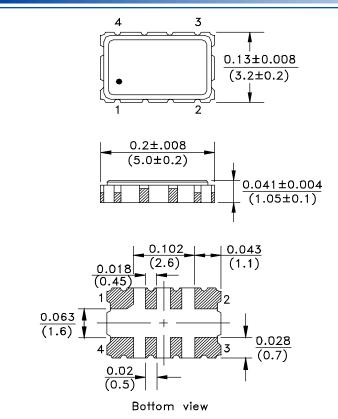
ASFLT SERIES



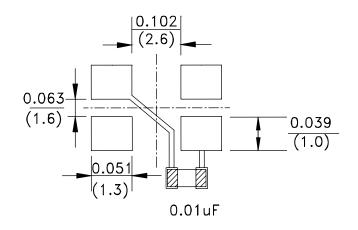


5.0 x 3.2 x 1.05mm

OUTLINE DRAWING:



Recommended land pattern



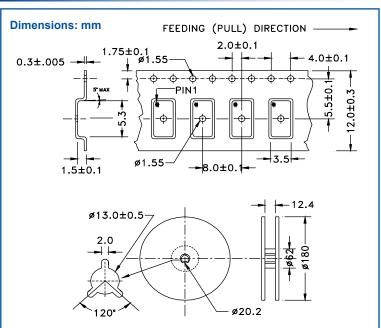
Resist layer is required on the path for the by-pass capacitor.

Note: It is recommended to use an approximately 0.01uF bypass capacitor between PIN 2 and 4.

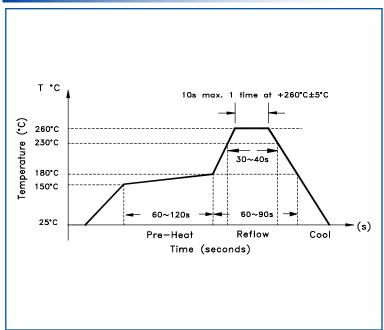
PIN	FUNCTION
1	CE
2	GND
3	Output
4	VDD

Dimensions: Inches (mm)

TAPE & REEL: (1,000 pcs/reel)



REFLOW PROFILE:



NOTE: Abracon manufactured products are intended for general commercial and industrial use. For applications requiring high reliability and/or presenting extreme operating environment, written consent & authorization from Abracon is required.



